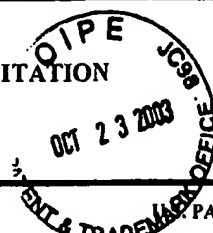


INFORMATION DISCLOSURE CITATION

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Docket Number (Optional)

13689

Application Number

09/870,800

Applicant(s)

Richard S. NORMAN et al.

Filing Date

June 1, 2001

Group Art Unit

2664

PATENT DOCUMENTS

EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
Hee	1.	U.S. 5,790,539	08-04-1998	Chao et al.			
Hee	2.	U.S. 5,831,980	11-03-1998	Varma et al.			
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Hee	5.	NOTANI H ET AL: "An 8*8 ATM switch LSI with shared multi-buffer architecture" PROCEEDINGS OF THE SYMPOSIUM ON VLSI CIRCUITS. SEATTLE, JUNE 4-6, 1992, SYMPOSIUM ON VLSI CIRCUITS, NEW YORK, IEEE, US, 4 June 1992 (1992-06-04), pages 74-75, XP010064987
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Docket Number (Optional)

13689

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Applicant(s)

Richard S. Norman et al.

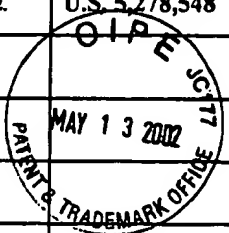
Filing Date

06/01/2001

Group Art Unit

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Hee	1.	U.S. 5,377,182	12/27/1994	Monacos			
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							YES	NO

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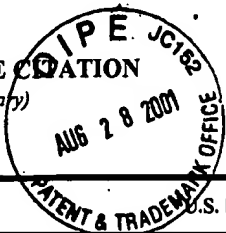
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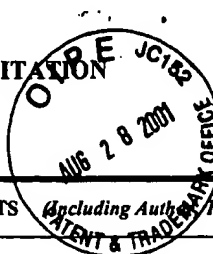
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Docket Number (Optional) 13689	Application Number 09/870,800
Applicant(s) Richard NORMAN et al.	
Filing Date June 1, 2001	Group Art Unit 2664

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Docket Number (Optional)

1368°

Application Number

Applicant(s)

Richard S. NORMAN et al.

Filing Date

Group Art Unit

31017 U.S. PTO
09/870800

06/01/01

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							YES	NO
Hee	12.	EP 0 680 173 A1	02/11/1995	EUROPE				

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Hee	13.	Nick McKeown et al. "The Tiny Tera: a Packet Switch Core"; Departments of Electrical Engineering and Computer Science, Stanford University; August 1996; http://tiny-tera.stanford.edu/tiny-tera/index.html						

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Richard S. NORMAN et al.

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11017 U.S. PTO
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